Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	183	magnetoresistance and ((metal or metaliz$5) with circuit$2) and (process$3 or cpu) and (interconnect$3 or interfac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:42
L2	73	magnetoresistance and ((metal or metaliz$5) with circuit$2) and (analog) and (interconnect$3 or interfac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:33
L3	72	magnetoresistance and ((metal or metaliz$5) with circuit$2) and (process$3 or cpu) and analog and (interconnect$3 or interfac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:33
L4	18	magnetoresistance and ((metal or metaliz$5) with circuit$2) and (multi-layer$1 or multilayer$1) and module$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:40
L5	90	magnetoresistance and ((metal or metaliz$5) with circuit$2) and stack$3 and (interconnect$4 or interfac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:41
L6	57	1 and ("365"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:41
L7	25	2 and ("365"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:41
L8	25	3 and ("365"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:41
L9	31	5 and ("365"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:42
L10	0	magnetoresistance and (all with (metal or metaliz$5) with circuit$2) and (process$3 or cpu) and (interconnect$3 or interfac$3)	US-PGPUB	OR	OFF	2005/10/11 15:43

| L11 | 0 | magnetoresistance and (all with (metal or metaliz$5) with circuit$2) and stack$3 and (interconnect$3 or interfac$3) | US-PGPUB | OR | OFF | 2005/10/11 15:45 |
| L12 | 0 | magnetoresistance and (all with (metal or metaliz$5) with circuit$2) | US-PGPUB | OR | OFF | 2005/10/11 15:45 |